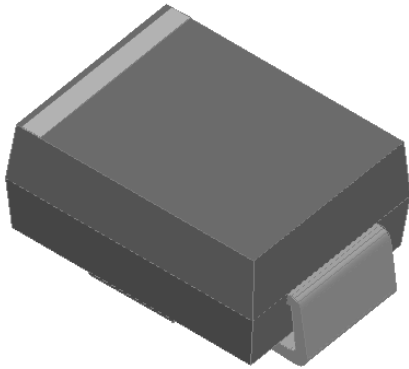




Surface Mount High Efficient Rectifier



Features

- Low profile package
- Ideal for automated placement
- Glass passivated chip junction
- High forward surge capability
- Super fast reverse recovery time
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

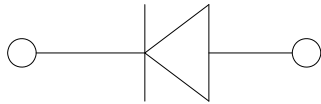
Typical Applications

For use in high frequency rectification of power

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Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free



- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** Cathode line denotes the cathode end

■Maximum Ratings (Ta=25 Unless otherwise specified

PARAMETER	SYMBOL									
	V_{RRM}	V	50	100	200	300	400	600	800	1000
Maximum RMS Voltage	V_{RMS}	V	35	70	140	210	280	420	560	700
Maximum DC blocking Voltage	V_{DC}	V	50	100	200	300	400	600	800	1000
Average rectified output current @60Hz sine wave, resistance load, T_L (Fig.1)	I_O	A	2.0							
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, $T_j = 25$	I_{FSM}	A	50							
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, $T_j = 25$			100							
Current squared time @1ms ≤ t ≤ 8.3ms $T_j = 25$ Rating of per diode	I^2t	A ² s	10.375							
Typical junction capacitance @Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	C_j	pF	32			17		12		
Storage temperature	T_{stg}		-55 ~ +150							
Junction temperature	T_j		-55 ~ +150							



US2A THRU US2M

■Electrical Characteristics $T_a=25$ Unless otherwise specified

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	US2A	US2B	US2D	US2F	US2G	US2J	US2K	US2M
Maximum instantaneous forward voltage drop per diode	V _F	V	I _{FM} =2.0A	1.0			1.3		1.7		



■Ordering Information (Example)

PREFERED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	
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■ Outline Dimensions

γ Suggested pad layout



Disclaimer

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